

Rectifier Diode Module

V_{RRM} 1200 to 2000V

 IFAV
 240 Amp

 IFRMS
 375 Amp

Features

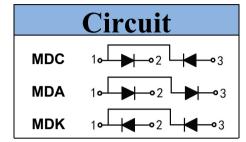
- Aluminum oxide DBC
- Glass passivated chip

Applications

- Non-controllable rectifiers for AC/DC
- Line rectifiers for transistorized AC motor
- Field supply for DC motors







	Type		V_{RRM}	V _{RSM}
MDC240GB-12	MDA240GB-12	MDK240GB-12	1200V	1300V
MDC240GB-16	MDA240GB-16	MDK240GB-16	1600V	1700V
MDC240GB-18	MDA240GB-18	MDK240GB-18	1800V	1900V
MDC240GB-20	MDA240GB-20	MDK240GB-20	2000V	2100V

Maximum Ratings

Symbol	Item	Conditions	Values	Unit	
I _{FAV}	Average Forward Current	180° Conduction Sin Half Wave, $T_c = 104$ °C	240	А	
I _{FRMS}	RMS Forward Current		375	Α	
I _{FSM}	Surge Forward Current	$T_j = 25^{\circ}C$, $t = 50Hz(10ms)$, $V_R = 0V$	8000	Α	
l ² t	Circuit Fusing Consideration	t = 10ms T _j =25°C	320000	A ² s	
V _{ISO}	Isolation Breakdown Voltage	AC 50Hz/60Hz; R.M.S; 1min	3000	V	
Tj	Operating Junction Temperature		-40 to +150	°C	
T _{stg}	Storage Temperature		-40 to +125	°C	
Mt	Mounting Torque	To Terminals(M6)	5±15%		
Ms	- Mounting Forque	To Heatsink(M6)	5±15%	N·m	
Weight	Module (Approximately)		185	g	

Thermal Characteristics

Symbol	Item	Conditions	Values	Unit
R _{th(j-c)}	Thermal Impedance, Max	Junction to Case(Per Diode)	0.13	°C/W
R _{th(c-s)}	Thermal Impedance, Max	Case to Heat Sink	0.05	°C/W

■ Electrical Characteristics

Symbol	Item	Conditions	Values			Unit
Symbol		Conditions	Min.	Тур.	Max.	Ullit
V_{FM}	Forward Voltage Drop, Max	$T_j = 25^{\circ}C$ $I_F = 720A$	_	_	1.45	V
I _{RRM}	Repetitive Peak Reverse Current, Max	$T_j = 25$ °C $V_R = V_{RRM}$	_	_	0.1	mA
		$T_i = 150$ °C $V_R = V_{RRM}$	_	_	15	
V _{T0}	Threshold Voltage, for power loss calculation only	T _j = 125°C	0.90		V	
r _T	Slope Resistance, for power loss calculation only	T _j = 125°C	0.77		mΩ	

Revised: 2024-04, Revision2.1



Performance Curves

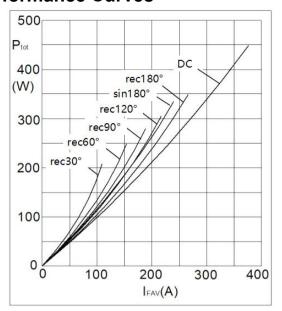


Fig1. Power Dissipation

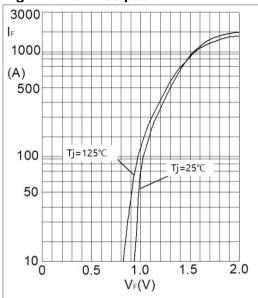


Fig3. Forward Characteristics

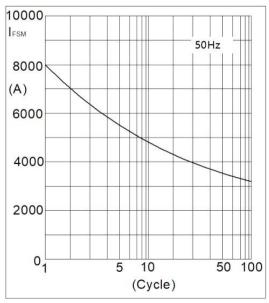


Fig5. Max Non-Repetitive Forward Surge Current

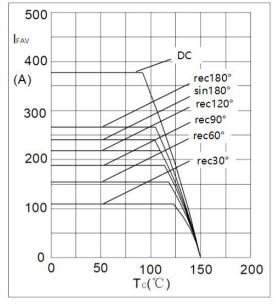


Fig2. Forward Current Derating Curve

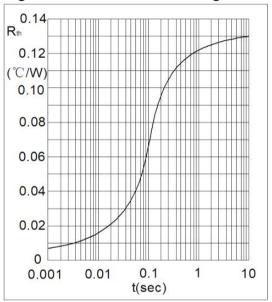
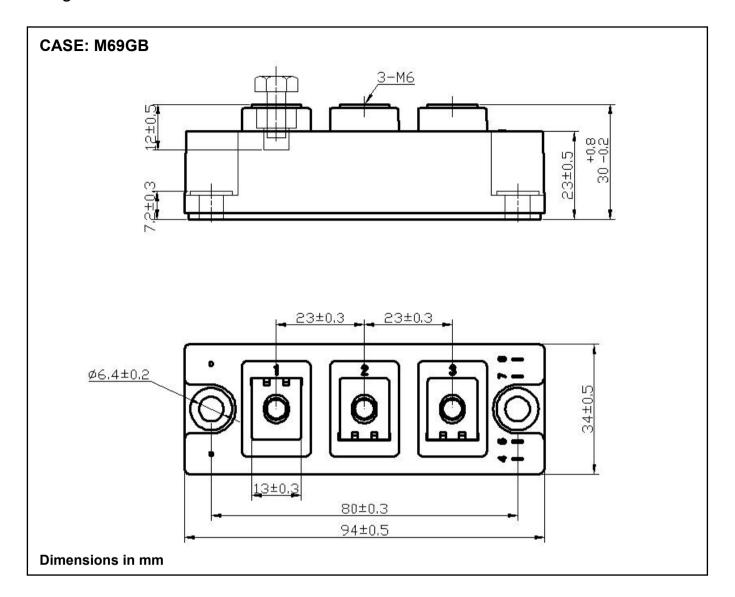


Fig4. Transient Thermal impedance



Package Outline Information



Revised: 2024-04, Revision2.1



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Revised: 2024-04, Revision2.1